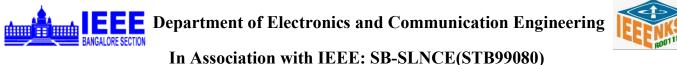




Sir. M Visvesvaraya College of Engineering, Raichur

"Circuit Prototyping"

Organized by



Attended: IEEE Members:10, Guests:30

A Student Development Program was organized for two days by Electronics and Communication Engineering department in association with IEEE Student Branch (STB99080) on **27-09-2024** & **28-09-2024** A Program begin with formal inauguration by Dr. Vishwanath P,HOD & Prof at **09: 15 am in LAB-3** of E&CE department.

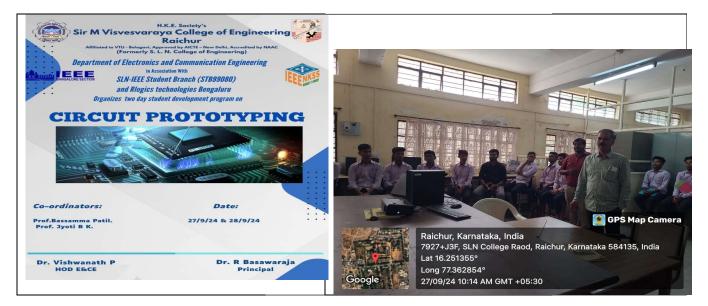
Dr. Vishwanath P, HOD, E&CE dept, inaugurated SDP .Dr. Sangamesh H, IEEE Student Branch counselor welcomed Resource Person Mr.Rakesh from Rlogics technologies, Bellary, Faculties of Department and all participants.

Prof. Jyoti B K and Prof. Bassamma Patil were the coordinators for the SDP. Participants are enthusiastically involved in learning Circuit Prototyping by building an actual circuit to a theoretical design. In the First half of the session the concept of theoretical approach is explained by a Resource Person and second half deals with the practical approach.

Such programs are necessary to enhance student's skills and develop the knowledge other than regular academic studies.

Finally Program ended by thanking a resource person Mr.Rakesh by Prof.Smita Chetti on behalf of faculties of Department and Participants.

Glimpses Student Development Program:







Instagram Link: <u>https://www.instagram.com/reel/DAqWA34v3ZU/?igsh=ZWY1Zzg4bXViMDJy</u>